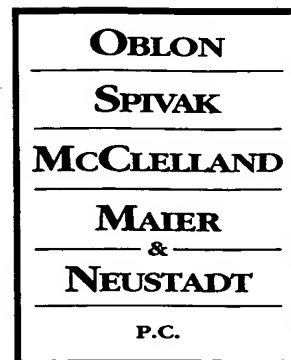




COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

Docket No.: 242278US2S DIV



ATTORNEYS AT LAW

RE: Application Serial No.: 10/662,309

Applicants: Ryo FUKUDA

Filing Date: September 16, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT
HAVING LATCHING MEANS CAPABLE OF
SCANNING

Group Art Unit: 2824

Examiner: LE, TOAN K.

SIR:

Attached hereto for filing are the following papers:

REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.111
TERMINAL DISCLAIMER

Our credit card payment form in the amount of \$130.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

Registration No. 28,870

Customer Number

22850

(703) 413-3000 (phone)

(703) 413-2220 (fax)

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